PTO/SB/08A (10-01)

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STA	TEMENT	BY AP	PLICANT	First Named Inventor	Donald A. Hitko		
				Group Art Unit			
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Sheet	1	of	2	Attorney Docket Number	03-1119		

			U.S. PATENT	DOCUMENTS	
Examiner Initials*	Cite No.	Document Number Number – Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Passages or Relevant Figures Appear
J.L	1	US- 5,394,122	February 28, 1995	Conway, et al.	
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Substitute	e for form 1449A/F	PTO		Complete if Known			
				Application Number			
INFO	DRMATION D	DISC	LOSURE	Filing Date			
STATEMENT BY APPLICANT				First Named Inventor	Donald A. Hitko		
				Group Art Unit			
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Sheet	2	of	2	Attorney Docket Number	03-1119		

	OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS	
Cite No. 1	item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
4	CECIL, J.B. AND HOWARD, J.G.; A Fully Monolithic 6-Bit, Multiplying D/A Converter; IEEE International Solid-State Circuits Conference Proceedings; Feb. 1971, p52-53.	
5	KRABBE, H. AND MOLINARI, F; A Monolithic Multiplying D/A Converter Circuit; IEEE International Solid-State Circuits Conference Proceedings; Feb. 1971, p54-55, 198.	
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